

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L35	352	(contiguous\$2 near3 (area\$1 or region\$1)) and command\$1 and graphic\$1	USPA T	2001/08/09 12:04
2	BRS	L38	0	36 and 37	USPA T	2001/08/09 12:06
3	BRS	L36	38	(contiguous\$2 near3 (area\$1 or region\$1)) and (graphic\$1 near3	USPA T	2001/08/09 12:06
4	BRS	L37	25	(contiguous\$2 near3 (area\$1 or region\$1)) and (encod\$3 near3	USPA T	2001/08/09 12:33
5	BRS	L40	15	(command\$1) and (interpreter\$1) and	USPA T	2001/08/09 12:40
6	IS&R	L41	3	(("6108737") or ("6205494") or ("6260098")) or ("6260098").PN.	USPA T	2001/08/09 12:43
7	IS&R	L42	2	(("6205494") or ("6260098")) or ("6260098").PN.	USPA T	2001/08/09 12:44
8	IS&R	L43	0	("buffer\$1 or file\$1) and command\$1 and	USPA T	2001/08/09 12:46
9	BRS	L44	20098	(buffer\$1 or file\$1) and command\$1 and	USPA T	2001/08/09 12:47
10	BRS	L45	723	parameter\$1 44 and (tag\$1 and bank\$1)	USPA T	2001/08/09 12:48
11	BRS	L46	357	45 and slot\$1	USPA T	2001/08/09 12:49
12	BRS	L47	216	44 and (status near3 array\$1)	USPA T	2001/08/09 12:49
13	BRS	L48	5	47 and (command near3 interpreter\$1)	USPA T	2001/08/09 12:51
14	BRS	L49	9	Graphic\$1 near3 command\$1 near3	USPA T	2001/08/09 12:56
15	BRS	L50	0	chln.in. and compaq.as.	USPA T	2001/08/09 12:57
16	BRS	L51	9	chln.in. and compaq.as.	USPA T	2001/08/09 13:01
17	BRS	L52	93	s3.as.	USPA T	2001/08/09 13:01
18	BRS	L53	1	52 and goettsch.in.	USPA T	2001/08/09 13:02
19	BRS	L55	0	52 and ho.in.	USPA T	2001/08/09 13:02
20	BRS	L56	0	52 and fong.in.	USPA T	2001/08/09 13:03
21	BRS	L54	5	52 and zhao.in.	USPA T	2001/08/09 13:03
22	IS&R	L57	1139	("710/52-57").CCLS.	USPA T	2001/08/09 13:08
23	BRS	L58	153	57 and graphic\$1	USPA T	2001/08/09 13:10
24	BRS	L59	138	57 and graphic\$1 and (tag\$1 or address or	USPA T	2001/08/09 14:47
25	IS&R	L60	11	(("6205494") or ("6260098") or ("5964859") or ("5905509") or ("5983315") or ("5623628") or ("5848297") or ("580593	USPA T	2001/08/09 14:48

	Document ID	Issue Date	Pages	Title	Current OR
1	US 6205494 B1	20010320	13	Controller with automatic generation of linked list of data transfer descriptors for sequential commands, with linked list being used for allocating post and prefetch buffers for bus bridges	710/5
2	US 5964859 A	19991012	9	System for FIFO informing the availability of stages to store commands which include data and virtual address sent directly from application	710/129
3	US 5805930 A	19980908	20	Cache arrangement including coalescing buffer queue for non-cacheable data	710/57
4	US 5664148 A	19970902	8	System and method for establishing priorities in transferring data in burst counts from a memory to a plurality of FIFO stages, each having a low, intermediate, and high	711/138
5	US 5983315 A	19991109	9	Shared peripheral controller	711/109
6	US 6260098 B1	20010710	12	Control apparatus for maintaining order and accomplishing priority	710/130
7	US 5848297 A	19981208	17	Access to multiple ports to a level 2 cache having distributed first	710/56
8	US 5905509 A	19990518	18	Method and apparatus for combining uncacheable write data into cache-line-sized write buffers	345/520
9	US 5561780 A	19961001	15	Computer system and method for maintaining memory consistency in a pipelined, non-blocking caching bus request queue	711/126
10	US 5623628 A	19970422	49	Methods and apparatus for caching data in a non-blocking manner using a	711/141
11	US 5671444 A	19970923	28		710/52

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